

PATENT

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| APPL NUM 10032564 | FILING DATE 01/02/2002 | CLASS 438 | SUBCLASS 3 | GAU 2823 2812 | EXAMINER <i>Estrada</i> |
| **APPLICANTS: Okuno Yasutoshi; Tsuzumitani Akihiko; Mori Yoshihiro; | | | | | |
| **CONTINUING DATA VERIFIED: | | | | | |
| ** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-030068 02/08/2001 | | | | | |
| PG-PUB | | DO NOT PUBLISH <input type="checkbox"/> | | RESCIND <input type="checkbox"/> | |
| Foreign priority claimed | | <input type="checkbox"/> yes <input type="checkbox"/> no | | ATTORNEY DOCKET NO | |
| 35 USC 119 conditions met | | <input type="checkbox"/> yes <input type="checkbox"/> no | | 740819-725 | |
| Verified and Acknowledged Examiners's initials | | | | | |
| TITLE : Method for fabricating semiconductor device | | | | | |

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-04)

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| <input type="checkbox"/> TERMINAL DISCLAIMER | Primary Examiner | Application Examiner |
| | PREPARED FOR ISSUE | |
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